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block buffer erasing flash memory

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[Dual buffer flash memory architecture with multiple operating modes](#)

US Pat. 5822245 - Filed Mar 26, 1997 - Almet Corporation

A second portion of the buffer transfer to main memory page in the flash ...
from the logic block 20 written into the selected first or second memory buffer ...

[Data storage method and data processing device using an erasure block buffer ...](#)

US Pat. 6571312 - Filed Feb 16, 2000 - Mitsubishi Denki Kabushiki Kaisha

... flash memory 101 the data pertaining to the current erasure block buffer 103.
... there is eliminated a necessity for erasing data from flash memory at ...

[Semiconductor disk system having a plurality of flash memories](#)

US Pat. 5693901 - Filed May 5, 1995 - Kabushiki Kaisha Toshiba

In this semiconductor disk system, whether block swapping is to be performed ...
erasing means for setting the flash memory in the erase operation mode by ...

[File structure for a non-volatile block-erasable semiconductor flash memory](#)

US Pat. 5752689 - Filed Dec 1, 1995 - Intel Corporation

A more frequently cycled block is referred to a hot block. ... Personal computer
system 40 also includes RAM buffer 62 and flash memory array 64. ...

[Block-erasable non-volatile semiconductor memory which tracks and stores the ...](#)

US Pat. 5544358 - Filed Mar 3, 1995 - Intel Corporation

A more frequently cycled block is referred to a hot block. ... Personal computer
system 40 also includes RAM buffer 62 and flash memory array 64. ...

[Write reduction in flash memory systems through ECC usage](#)

US Pat. 5754567 - Filed Oct 15, 1998 - Micron Quantum Devices, Inc.

A preferred embodiment of flash memory array 30 is FIG. 1a block ... Computer
system 20 45 erasing or programming of the other cells is negligible. ...

[Flash EEPROM main memory in a computer system](#)

US Pat. 5696929 - Filed Oct 3, 1995 - Intel Corporation

Flash hard drive memo- valid data written from the cache buffer, erasing the ...
Important. in the holding buffer to the addressed block of flash memory. ...

[Flash memory mass storage system](#)

US Pat. 5933388 - Filed Apr 27, 1998 - Macronix International Co., Ltd.

The programming and erasing of such , a memory cell require current to pass ...
The old block replaced by the buffer can be erased and then used for other ...

[Flash memory](#)

US Pat. 5327384 - Filed Dec 28, 1992 - Nec Corporation

According to the invention, a flash memory includes: means for erasing data ...
buffer 10, a block selecting gate 18 connected to the block decoder 12, ...

[Multiple bank simultaneous operation for a flash memory](#)

US Pat. 6240040 - Filed Mar 15, 2000 - Advanced Micro Devices, Inc., Fujitsu Limited

Considering the flash memory 200 as illustrated from an addressing perspective
in FIG. ... Of course, the address buffer block 220, the logic circuit 218, ...

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